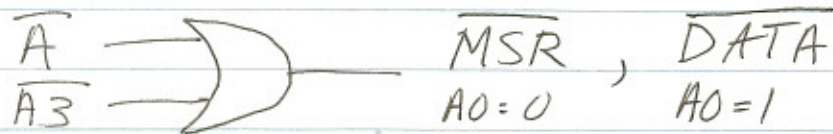


Address Decoder 10 ports \$3x Base

$$\overline{A} = \overline{A7} + \overline{A6} + A5 + A4 + \overline{IORQ}$$

$\overline{MSR} = \$36$ FDC MSR chip select

$\overline{DATA} = \$37$ FDC Data Reg chip select



$\overline{LATCH} = \$3A$



$\overline{DMA} = \$3C$

